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**INFORMATION DISCLOSURE  
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Sheet 1 of 1

Complete if Known

<b>Application Number</b>	09/945512
<b>Filing Date</b>	August 30, 2001
<b>First Named Inventor</b>	Forbes, Leonard
<b>Group Art Unit</b>	2812
<b>Examiner Name</b>	Booth, Richard

Attorney Docket No: 1303.027US1

**US PATENT DOCUMENTS**

Examiner Initials*	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
RB	US-5,952,692	09/14/1999	Nakazato, K., et al.	257	321	10/28/1997
RB	US-6,210,999	04/03/2001	Gardner, , et al.	438	183	12/04/1998
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**FOREIGN PATENT DOCUMENTS**

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**OTHER DOCUMENTS - NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T
RB		SHI, Y., "Tunneling Leakage Current in Ultrathin (<4 nm) Nitride/Oxide Stack Dielectrics", IEEE Electron Device Letters, 19(10), (1998),pp. 388-390	
RB		ZHANG, "Atomic Layer Deposition of High Dielectric Constant Nanolaminates", Journal of The Electrochemical Society, 148(4),(2001),F63-F66	

EXAMINER

DATE CONSIDERED

1/18/05

Information Disclosure Statement Form (PTO-164)

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